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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/813,254	03/30/2004	Gustavo J. Mehas	INSL.0104	4820	
26122	7590 04/14/2005		EXAM	EXAMINER	
LAW OFFICES OF GARY R. STANFORD 330 W OVERLOOK MOUNTAIN RD			ZWEIZIG, JEFFERY SHAWN		
BUDA, TX		AIN KD	ART UNIT	PAPER NUMBER	
			2816		
			DATE MAILED: 04/14/2009	DATE MAILED: 04/14/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	100				
	10/813,254	MEHAS ET AL.	(64)				
Office Action Summary	Examiner	Art Unit					
	Jeffrey S. Zweizig	2816					
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the d	correspondence add	ress				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	I36(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this comes (D) (35 U.S.C. § 133).	munication.				
Status							
1) Responsive to communication(s) filed on 25 C	October 2004.						
2a) ☐ This action is FINAL . 2b) ☐ This	s action is non-final.						
	Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4) ☐ Claim(s) 1-22 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) ☐ Claim(s) 9-22 is/are allowed. 6) ☐ Claim(s) is/are rejected. 7) ☐ Claim(s) 1-8 is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	wn from consideration.						
Application Papers							
9)☐ The specification is objected to by the Examine							
10)⊠ The drawing(s) filed on <u>25 October 2004</u> is/are		-	•				
Applicant may not request that any objection to the		` ,					
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	• • • • • • • • • • • • • • • • • • • •	•	` ,				
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in Applicat rity documents have been receiv u (PCT Rule 17.2(a)).	ion No ed in this National S	tage				
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate	152)				
Palent and Trademark Office							

Allowable Subject Matter

1. The Prior Art of record does not appear to disclose a margining circuit comprising the amplifiers and resistors as configured and operating as recited in claims 1 or 17 or the pins, divider, amplifiers and select logic configured as recited in claim 9.

Ex parte Quayle

2. This application is in condition for allowance except for the following formal matters:

Referring to Fig. 1 and claims 1-8, there appears to be some confusion with the connection order of the first and second margining nodes and the surrounding components. For example, claim 1 appears to define the circuit in "mode B" where as claim 2 appears to define the circuit in "mode A". Please review claims 1-8 to ensure consistent terminology usage through out.

Also, claims 1-8 appear to use the phrase "reference voltage" to describe the voltages at nodes VREF, OFS+ and OFS-. While these voltages are sometimes equal, they are three different voltages at three different circuit points and should have individual designations.

Prosecution on the merits is closed in accordance with the practice under *Ex* parte Quayle, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO**MONTHS from the mailing date of this letter.

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Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeffrey S. Zweizig whose telephone number is (571) 272-1758. The examiner can normally be reached on Monday thru Thursday 6:00 am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jeffrey S. Zweizig Primary Examiner Art Unit 2816